

REMARKS

Claims 9, 11, 14-17, 2-21 and 32-34 stand rejected in the above application. Claims 12, 13, and 18-19 are objected to as depending from a rejected independent claim. Amended independent claims 9 and 32 have been rewritten to recite the features of claims 16 and 17, and include features absent from Yamaguchi et al. (U.S. Patent No. 6,943,856), and therefore should be allowable.

The invention of amended claim 9 is characterized in that the structure provided beneath the reflecting plate and having unevenness is formed in the same layer as at least any one of the signal wire, gate wire and storage capacitor of the TFT substrate, or part of the structure is formed in the same layer as at least one of the signal wire and the gate wire of the TFT substrate.

Yamaguchi et al. (U.S. Patent No. 6,943,856) neither teaches nor suggests these features. The uneven structure of Yamaguchi is made of resin. Yamaguchi neither teaches nor suggests forming the uneven structure in the same layer as a wire layer.

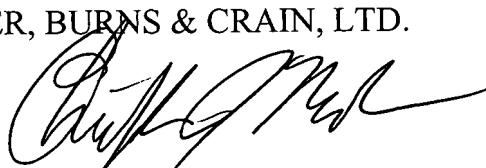
The features of the present invention require no separate resin layer and form the uneven structure in one of the wire layers. The invention thus requires no extra manufacturing steps, which is a remarkable advantage (see Applicants' specification pg. 15, lns. 16-21).

For all of the foregoing reasons, Applicants submit that this Application is in condition for allowance, which is respectfully requested. The Examiner is invited to contact the undersigned attorney if an interview would expedite prosecution.

Respectfully submitted,

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